



S/N 10/774,956 **EXPEDITED PROCEDURE - EXAMINING GROUP 2813**

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Donald S. Gardner	Examiner:	Tuan H. Nguyen
Serial No.:	10/774,956	Group Art Unit:	2813
Filed:	February 9, 2004	Docket No.:	884.140US3
Title:	INDUCTORS FOR INTEGRATED CIRCUITS (As Amended)		
Assignee:	Intel Corporation	Customer No:	21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Office Action mailed on December 5, 2005. Please amend the above-identified patent application as follows.

Intel
TN 1/17/06